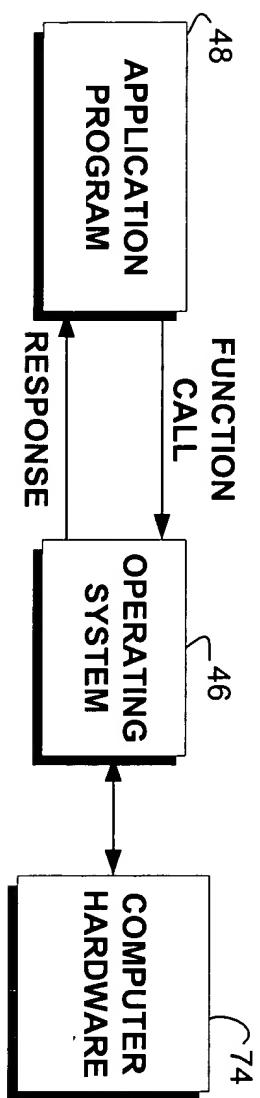


FIG.2.



48
FUNCTION
CALL
46
74

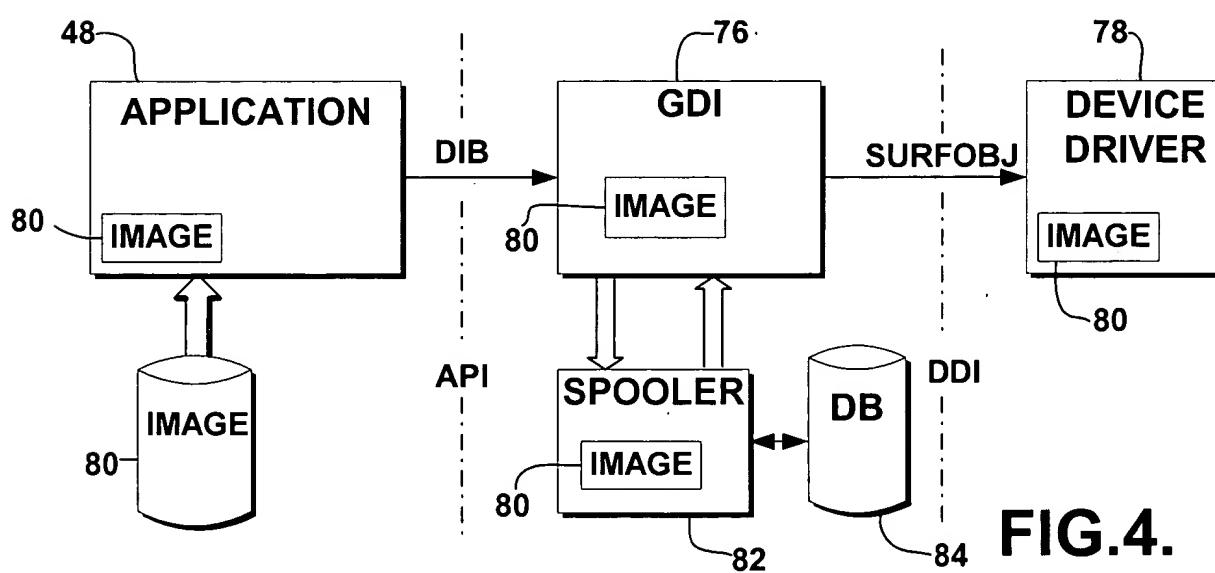
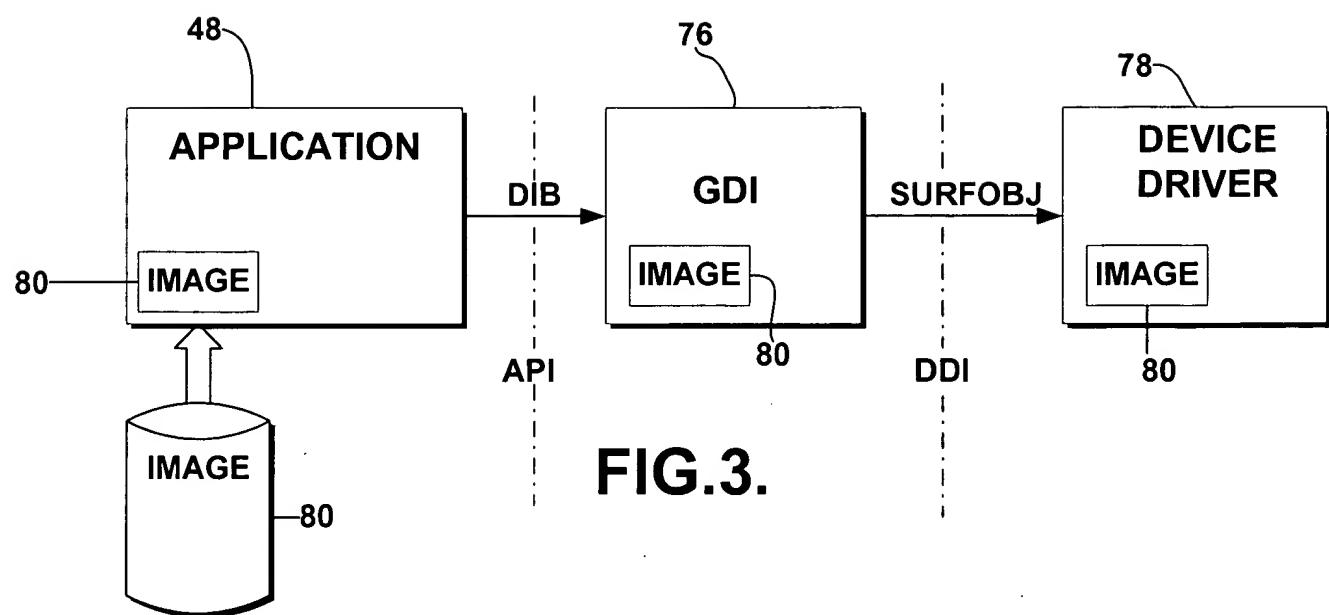


FIG. 5.

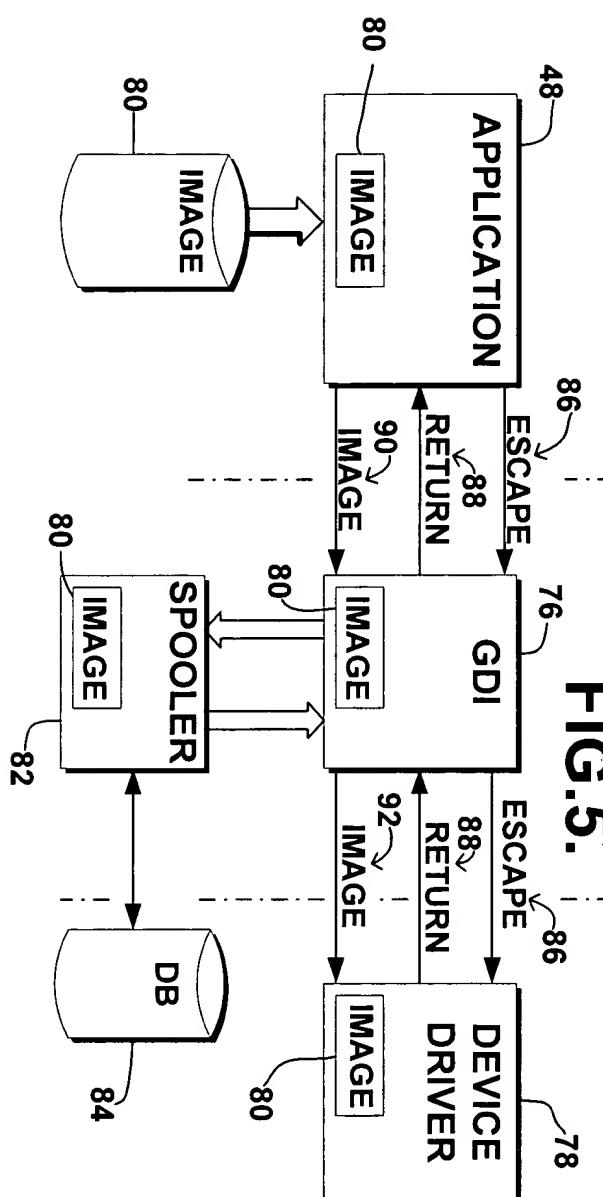


FIG.6.

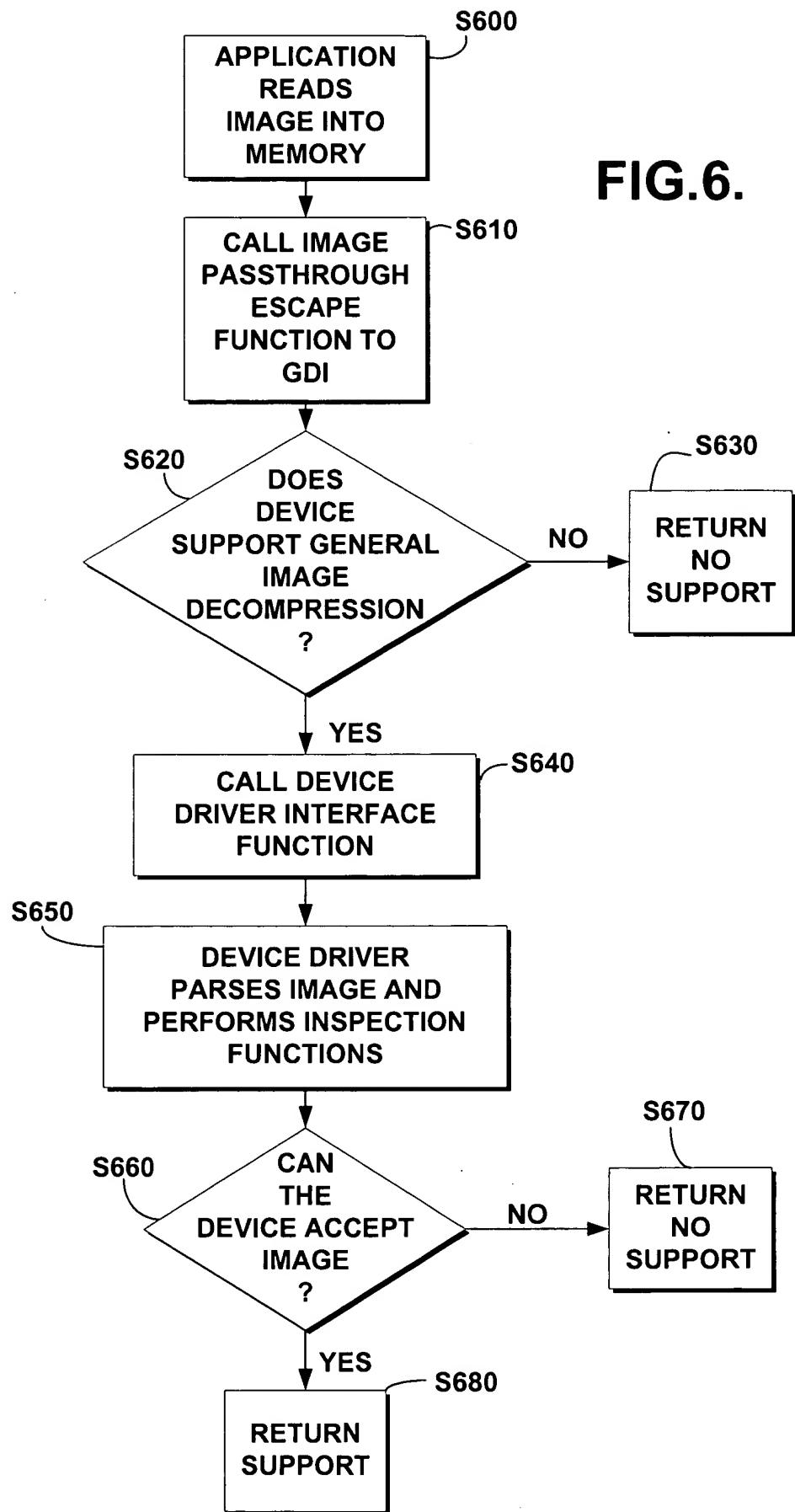


FIG.7.

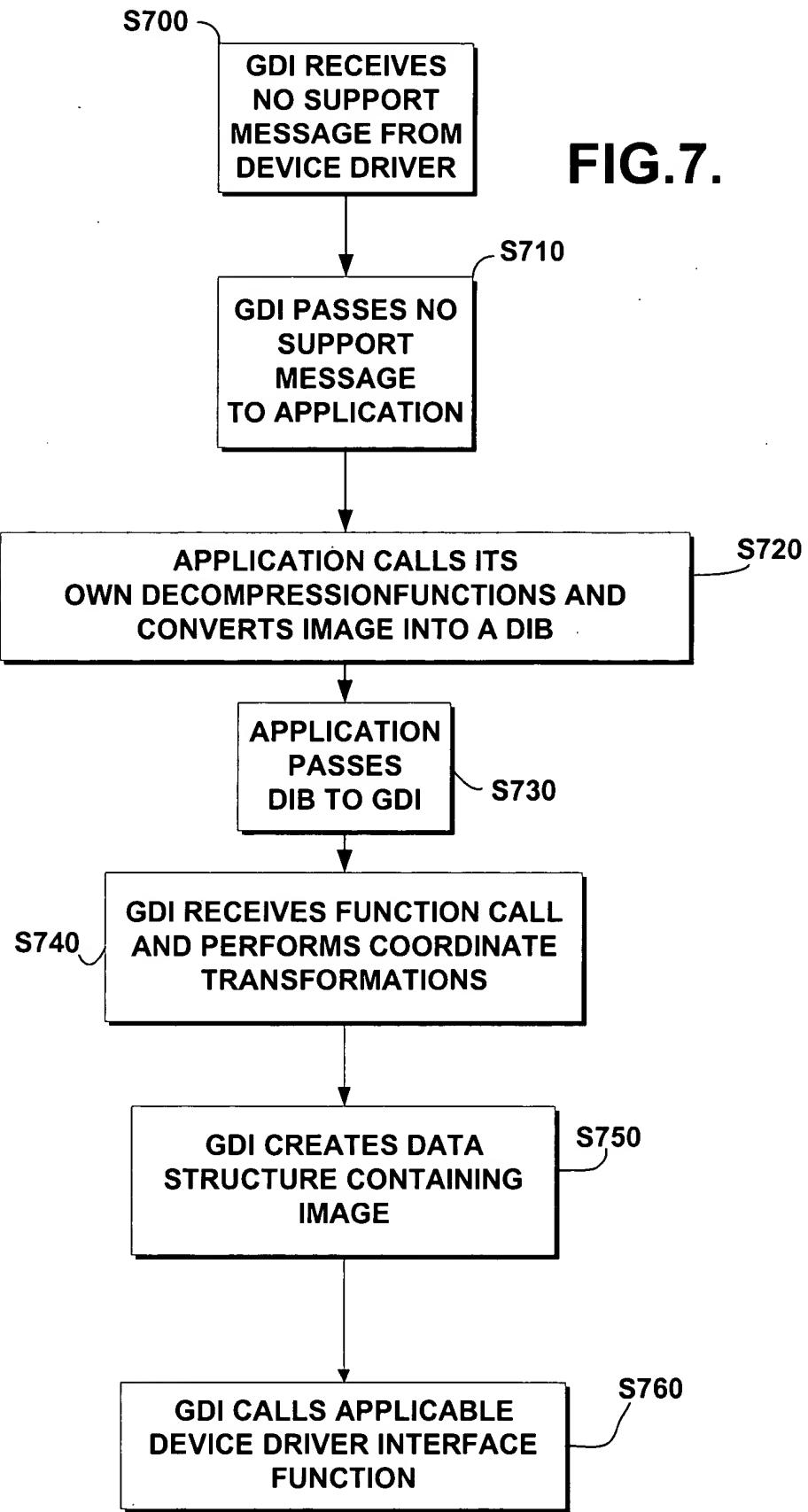
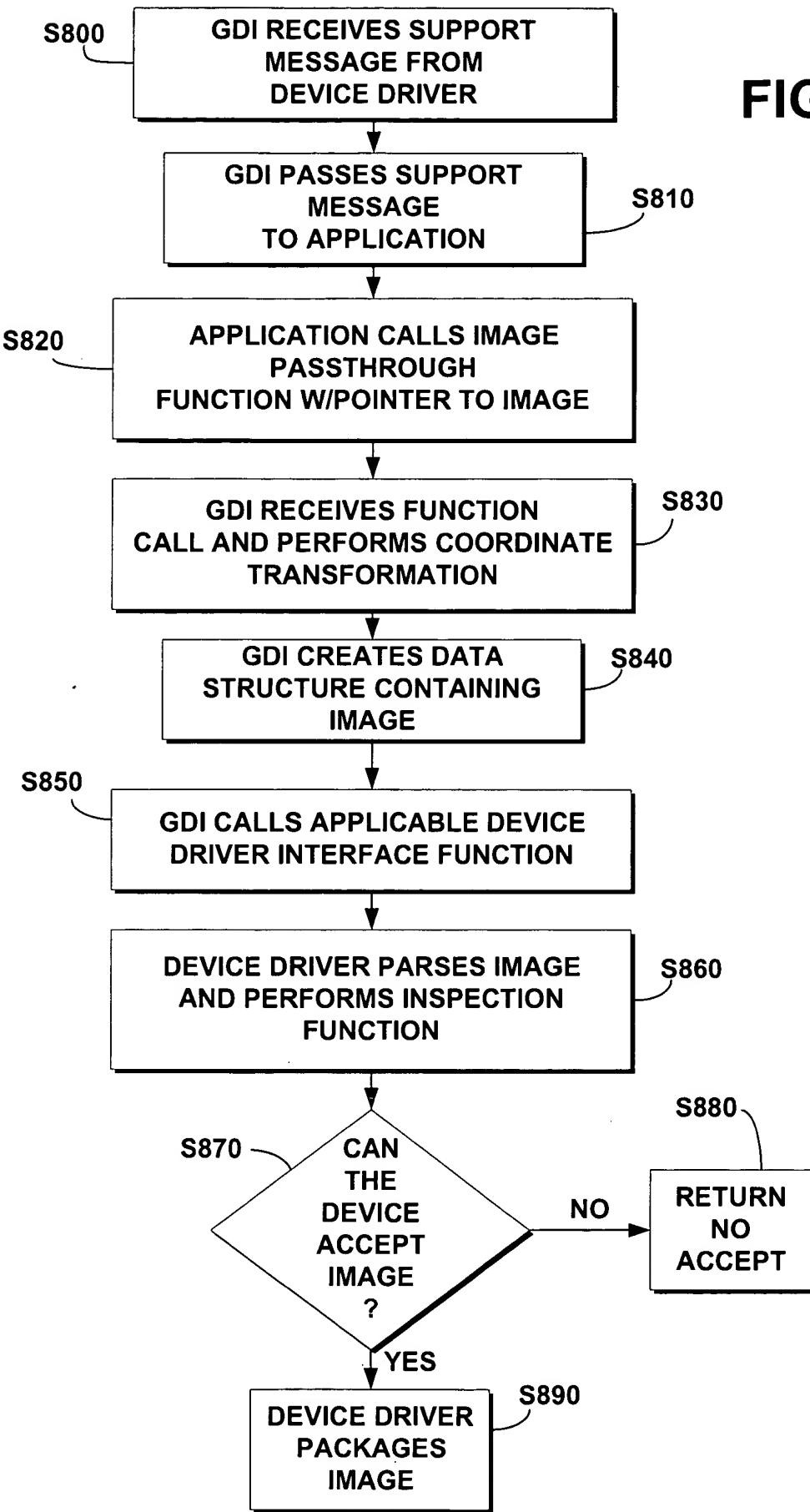


FIG.8.



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EXTESCAPE	hdc	nESCAPE	cbInput	lpzInData	CbOutput	lpzOutData
95	94	96	98	100	102	93

The diagram illustrates the memory layout of two structures, `DEVINFO` and `fIGraphicsCaps2`. The `DEVINFO` structure is at address 104, and the `fIGraphicsCaps2` structure is at address 106. A bracket above the structures indicates they are part of the same contiguous memory block.

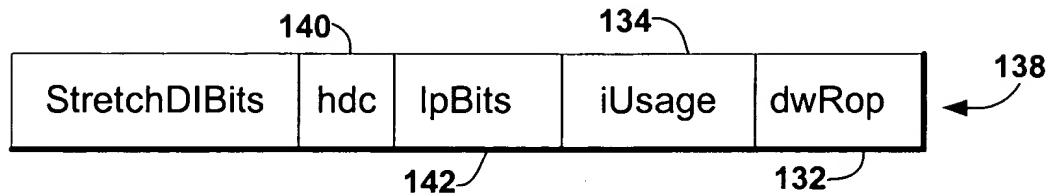
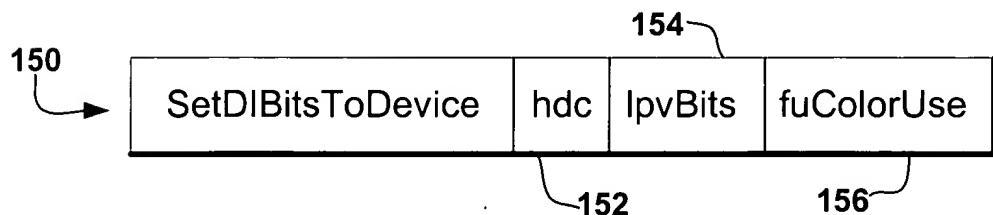
104	DEVINFO
106	<code>fIGraphicsCaps2</code>

FIG 10.

EIG-2

```

graph TD
    BSI[BiSizeImage] --- biHeight[biHeight]
    BSI --- biBitCount[biBitCount]
    BSI --- biWidth[biWidth]
    BSI --- biSizelImage[biSizelImage]
    biHeight --- offset130[130]
    biSizelImage --- offset132[132]
    biWidth --- offset134[134]
    biBitCount --- offset136[136]
    
```

FIG.13.**FIG.14.****FIG.15.**